

IN THE SPECIFICATION

Please amend the Abstract of the Disclosure on page 21 of the application as follows:

A method and system for reducing power consumption in digital circuits using charge redistribution, comprising include a plurality of signal lines, an intermediate floating virtual source/sink, and a charge redistribution circuit connected to each [[said]] signal line that isolates [[said]] the signal line from its source and connects it to the intermediate floating virtual source/sink during an idle period prior to a change of state.

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—This charge redistribution provides steady state statistical independent advantage due to charge recycling without inserting extra complimentary lines.